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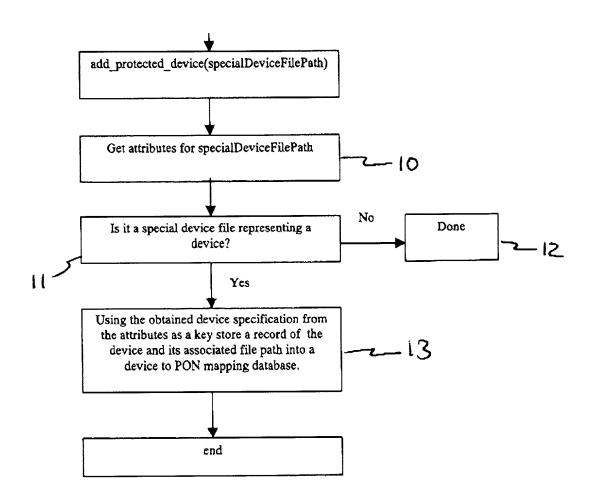


Figure 1



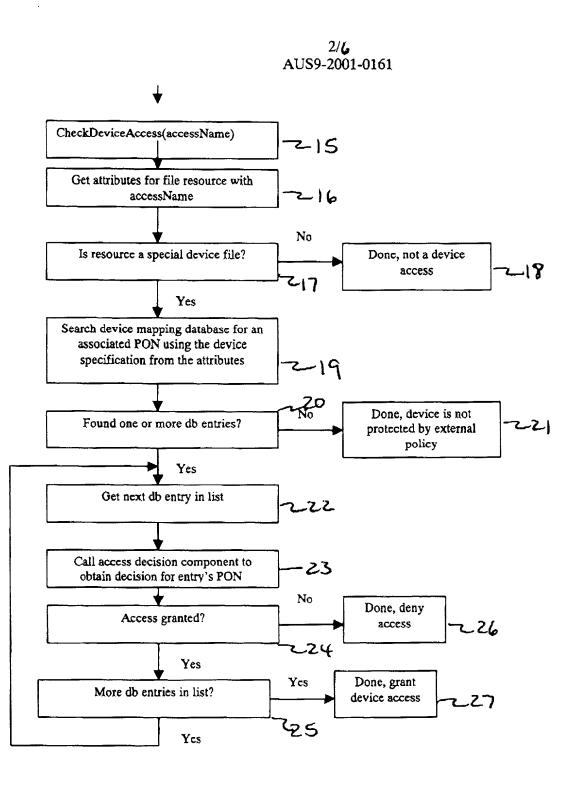


Figure Z

3/6 AUS9-2001-0161

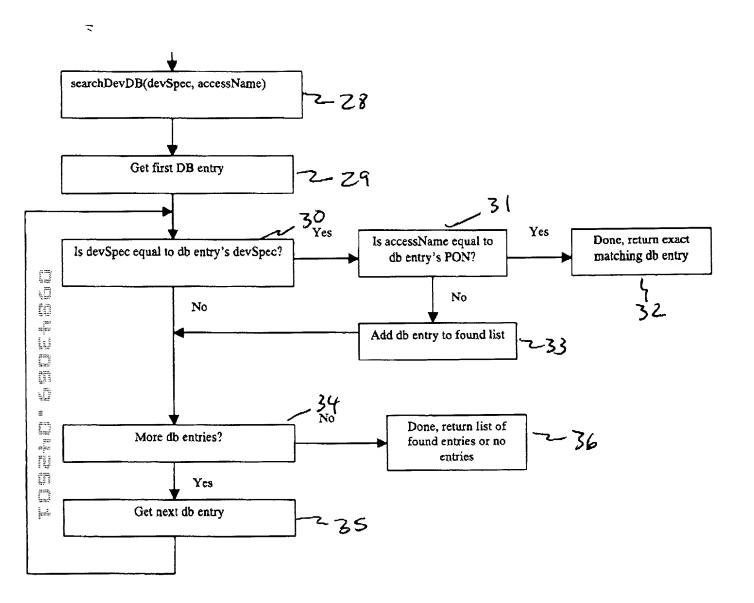


Figure 3

MAR 29 2001 11:36 7137721255 PAGE.03

4**/6** AUS9-2001-0161

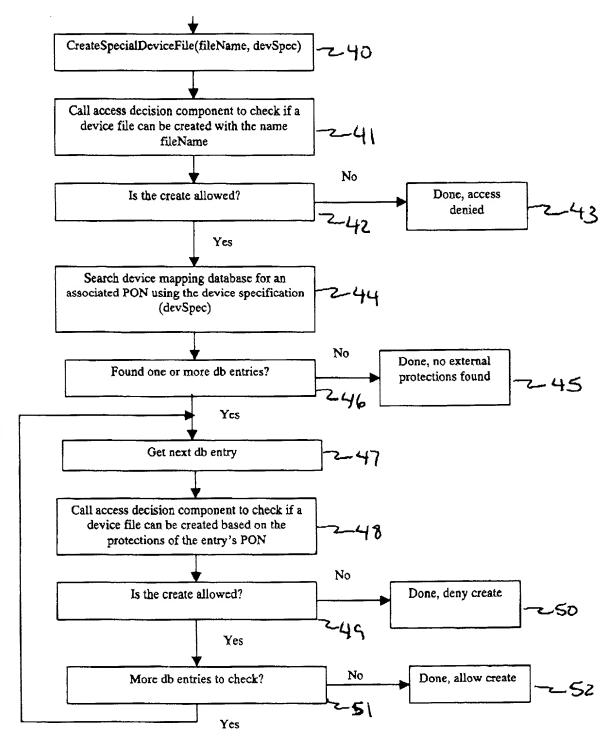


Figure 4

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Example High Level Architecture Relationship Between an External Authorization Manager and the Described Device Security Mechanism

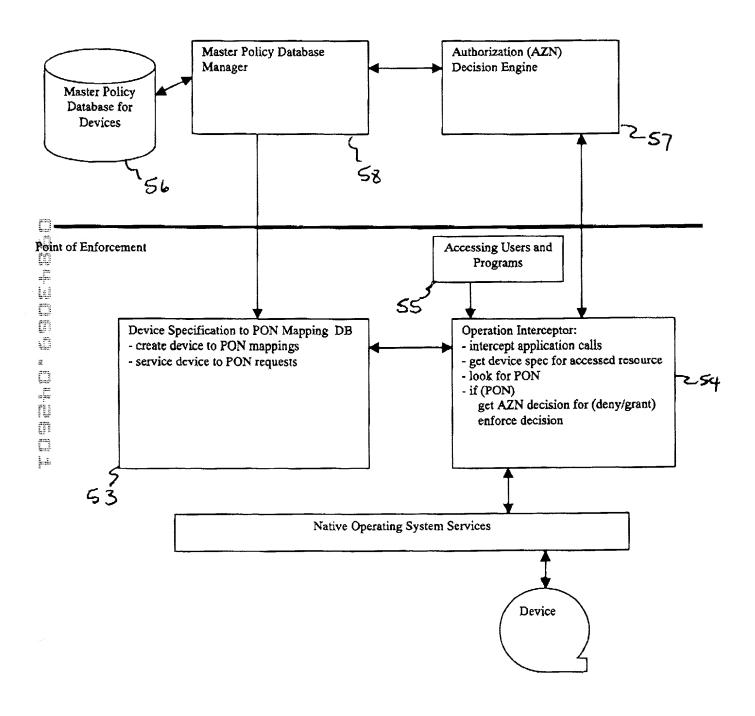


Figure 5

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6/6 AUS9-2001-0161

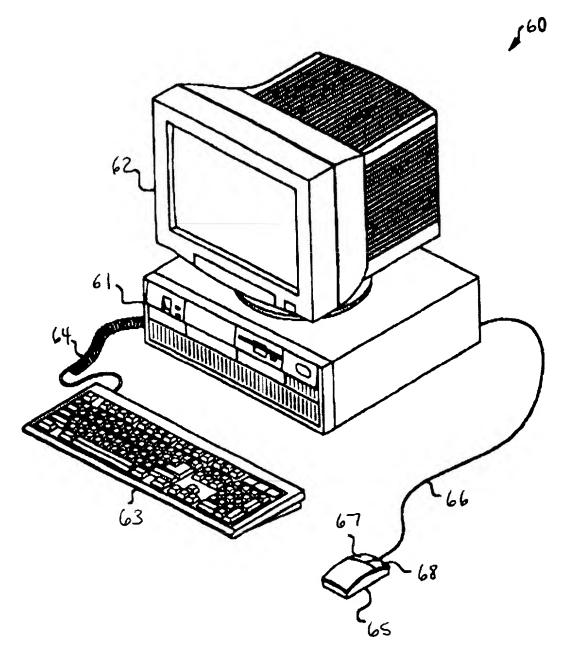


Figure 6

PAGE.06 7137721255 MAR 29 2001 11:37